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Term	Documents
SHARE.DWPI,TDBD,EPAB,JPAB,USPT.	82849
SHARES.DWPI,TDBD,EPAB,JPAB,USPT.	19889
SHAR???	0
SHARABA.DWPI,TDBD,EPAB,JPAB,USPT.	2
SHARABI.DWPI,TDBD,EPAB,JPAB,USPT.	31
SHARABY.DWPI,TDBD,EPAB,JPAB,USPT.	101
SHARADA.DWPI,TDBD,EPAB,JPAB,USPT.	11
SHARADD.DWPI,TDBD,EPAB,JPAB,USPT.	1
SHARAEV.DWPI,TDBD,EPAB,JPAB,USPT.	32
SHARAFY.DWPI,TDBD,EPAB,JPAB,USPT.	1
((SHARE OR SHAR??? OR SHARED) ADJ3 SYNCHRONOUS ADJ3 MEMORY ).USPT,JPAB,EPAB,DWPI,TDBD.	5

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USPT,JPAB,EPAB,DWPI,TDBD	(share or shar??? or shared) adj3 synchronous adj3 memory	5	<u>L16</u>
USPT,JPAB,EPAB,DWPI,TDBD	(master or base) same clock same (shared adj2 memory) same (synchronous or synchronize or synchronization or synchronizing)	25	<u>L15</u>
USPT,JPAB,EPAB,DWPI,TDBD	112 and (shared adj2 memory)	8	<u>L14</u>
USPT,JPAB,EPAB,DWPI,TDBD	(processor adj4 (provide or provid??? or transfer or send or send??? or transfer???? or supply or supplying or supplies) adj4 clock) same (synchronize or synchronous or synchronization)	34	<u>L13</u>
USPT,JPAB,EPAB,DWPI,TDBD	processor adj4 (provide or provid??? or transfer or send or send??? or transfer???? or supply or supplying or supplies) adj4 clock	345	<u>L12</u>
USPT,JPAB,EPAB,DWPI,TDBD	processor same (provide or provid??? or transfer or send or send??? or transfer???? or supply or supplying or supplies) same clock	11080	<u>L11</u>
USPT,JPAB,EPAB,DWPI,TDBD	4965717[uref]	87	<u>L10</u>
USPT,JPAB,EPAB,DWPI,TDBD	4965717.pn.	3	<u>L9</u>
USPT,JPAB,EPAB,DWPI,TDBD	16 same memory	51	<u>L8</u>
USPT,JPAB,EPAB,DWPI,TDBD	16 and l2	15	<u>L7</u>
USPT,JPAB,EPAB,DWPI,TDBD	(share or shared or shar???) adj2 clock	493	<u>L6</u>
USPT,JPAB,EPAB,DWPI,TDBD	(synchronize or synchronous or synchroniz???) same clock same shared same memory same (agent or processor or device or cpu)	95	<u>L5</u>
USPT,JPAB,EPAB,DWPI,TDBD	5999197[uref]	1	<u>L4</u>
USPT,JPAB,EPAB,DWPI,TDBD	shared same synchronous same memory same (processor or agent or device or cpu or system) same clock	58	<u>L3</u>
USPT,JPAB,EPAB,DWPI,TDBD	shared same synchronous same memory	401	<u>L2</u>
DWPI,USPT,EPAB,JPAB,TDBD	shared adj2 synchronous adj2 memory	4	<u>L1</u>